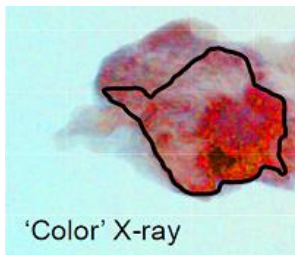




Caeleste Newsletter,
January 2012

Caeleste and UZB presentation at RSNA conference available for download



'Color' X-ray

UZB presented at the RSNA conference in Chicago (30 Nov 2011) the results of its work in partnership with Caeleste on the diagnosis advantage of Color X-ray in Breast Cancer detection. We are pleased to make the presentation available on our website:

[Pdf Abstract](#) and [presentation](#) available for download.

Caeleste publishes 0.5 e⁻_{RMS} noise CMOS pixel



Caeleste published last month (6-7 December 2011) its first measurement results of a 0.5 e⁻_{RMS} noise CMOS image sensor at the occasion of the CNES Workshop on High performance CMOS image sensors in Toulouse, France. Starting from a 2 e⁻_{RMS} baseline obtained with in-pixel CTIA amplifier, the noise is further reduced by oversampling of uncorrelated pixel data. De-correlation is achieved by 1/f noise "resetting function". The presentation is available for download as pdf [here](#).

ESO and Caeleste paper at the CNES workshop available for download



ESO published results of a joint project with e2v and Caeleste at the CNES conference on High performance CMOS image sensors. It describes a "High QE, Thinned Backside-Illuminated, 3e⁻ RoN, Fast 700fps, 1760x1760 Pixels Wave-Front Sensor Imager with Highly Parallel Readout"

With courtesy of [ESO](#), presentation is available for download from [this link](#).

Caeleste introduces SPAD technology in standard CMOS process



Caeleste successfully designed and tested a SPAD (single photon avalanche diode) photon counting image sensor. After a first successful demonstration with [hybrid avalanche photodiode](#), Caeleste introduces integrated, compact pixels with avalanche photodiodes, within the CMOS circuitry [\[read more\]](#)